

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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FILED UNDER 35 U.S.C. §

PATENT NUMBER

U.S. **UTILITY** Patent Application

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| <p>O.I.P.E.</p> <p>AK</p> <p>SCANNED MS ② Q.A. AA</p> | <p>PATENT DATE</p> |
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| APPLICATION NO. 09/719422 | CONT/PRIOR D F | CLASS 156 | SUBCLASS 242 | ART UNIT 1734 1733 | EXAMINER Musser |
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| TITLE | APPLICANTS |
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| 92. Member | 92. Mr. J. H. ... |
| 93. Member | 93. Mr. J. H. ... |
| 94. Member | 94. Mr. J. H. ... |
| 95. Member | 95. Mr. J. |

Yoshihisa Furuta
Yoshinori Watanabe

Semiconductor chip resin-sealing method and adhesive tape for pasting lead frames or the like

PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | | | | | | |
|------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|--|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Primary Examiner) (Date) | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |

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